UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,798,058 B1

DATED : September 28, 2004 INVENTOR(S) : Nobuaki Hashimoto

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page,

Item [54], Title, should read -- SEMICONDUCTOR DEVICE, MOUNTING SUBSTRATE AND METHOD OF MANUFACTURING MOUNTING SUBSTRATE, CIRCUIT BOARD, AND ELECTRONIC INSTRUMENT --

Signed and Sealed this

Twelfth Day of April, 2005

JON W. DUDAS
Director of the United States Patent and Trademark Office